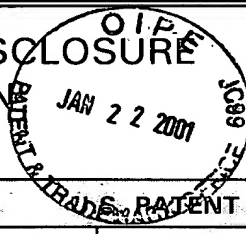
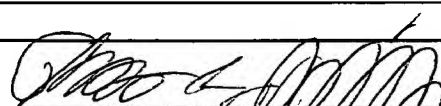


INFORMATION DISCLOSURE CITATION PTO-1449				ATTORNEY'S DKT NO. 026350-028		APPLICATION NO. 09/389,321	
				APPLICANT Toshiro HIRAMOTO et al		# 6	
				FILING DATE September 3, 1999		GROUP 2811	



PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
TO	JP9-162417	06/20/97	Japan (with English abstract)				
TO	JP7-131025	05/19/95	Japan (with English abstract)				
TO	JP10-256556	09/25/98	Japan (with English abstract)				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
TO	F. Assaderaghi et al, "A Dynamic Threshold Voltage MOSFET (DTMOS) for Ultra-Low Voltage Operation", IEEE, 1994, 33.1.1 - 33.1.4
EXAMINER	
DATE CONSIDERED	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.